

**ABSTRACT OF THE DISCLOSURE**

A system for performing data error recovery includes a memory unit and a memory controller. The memory unit includes a plurality of memory locations, and the  
5 memory controller maintains a checksum in one of the memory locations. At various times, the memory controller receives requests to update the checksum with data values identified by the requests. In response, the memory controller combines the checksum with these data values and stores the foregoing data values into memory. In  
one embodiment, the memory controller stores the foregoing data values into a  
10 plurality of stacks based on which protection domains are associated with the data values. In response to a detection of a data error, the memory controller retrieves a plurality of the stored data values and recovers a previous state of a particular memory location by combining each of the retrieved data values to the checksum.